

AMENDMENTS TO THE CLAIMS

This listing of claims will replace all prior versions and listing of claims in the application.

Listing of Claims:

1. (Cancelled)

2. (Currently Amended) A storage system ~~according to claim 1, further comprising:~~

a storage device;

a first controller and a second controller both connected to the storage device and a host system,

a first power unit; and

a second power unit,

wherein the first controller has a first memory, said first memory being a cache memory and a second memory,

wherein, the second controller has a third memory, said third memory being a cache memory that receives a copy of the data received by said first memory,

wherein, in the case where the first controller receives data from the host system, the first controller stores the data in the first and the second memories and sends a response to the host system, and then the first controller transfers the data stored in the second memory to the third memory,

_____ wherein the first memory of the first controller receives power feeding from the first power unit,

_____ wherein the second memory of the first controller and the third memory of the second controller receive power feeding from the second power unit.

3. (Currently Amended) A storage system ~~according to claim 1~~, further comprising:

a storage device;

a first controller and a second controller both connected to the storage device and a host system,

a first power unit;

a second power unit; and

a third power unit,

wherein the first controller has a first memory, said first memory being a cache memory and a second memory,

wherein the second controller has a third memory, said third memory being a cache memory that receives a copy of the data received by said first memory,

wherein, in the case where the first controller receives data from the host system, the first controller stores the data in the first and the second memories and sends a response to the host system, and then the first controller transfers the data stored in the second memory to the third memory,

_____ wherein the first memory of the first controller receives power feeding from the first power unit,

wherein the second memory of the first controller receives power feeding from the second power unit,

wherein the third memory of the second controller receives power feeding from the third power unit.

4. (Currently Amended) A storage system ~~according to claim 1~~, further comprising:

a storage device;

a first controller and a second controller both connected to the storage device and a host system; and

a power unit,

wherein the first controller has a first memory, said first memory being a cache memory and a second memory,

wherein the second controller has a third memory, said third memory being a cache memory that receives a copy of the data received by said first memory,

wherein, in the case where the first controller receives data from the host system, the first controller stores the data in the first and the second memories and sends a response to the host system, and then the first controller transfers the data stored in the second memory to the third memory,

wherein the first memory of the first controller and the second memory of the first controller receive power feeding from the power unit,

wherein the second memory of the first controller includes a battery and charges the battery by the use of the power unit.

5. (Original) A storage system according to claim 4,
wherein, in the case where a failure occurs in the power unit, the second memory switches the power feeding from the power unit to power feeding from the battery.
6. (Original) A storage system according to claim 2, wherein the second memory is a FIFO buffer.
7. (Original) A storage system according to claim 6, wherein the storage device is a plurality of storage devices.
8. (Original) A storage system according to claim 7, wherein the second memory has a unit for indicating presence or absence of data stored in the second memory.
9. (Currently Amended) A storage system comprising:
 - a host interface unit connected to a host system;
 - a switching unit connected to the host interface unit;
 - a first and a second controller connected to the switching unit; and
 - a storage device connected to the first and the second controller,wherein the first controller has a first memory, said first memory being a cache memory, and a second memory,

wherein the second controller has a third memory, said third memory being a cache memory that receiving-receives a copy of data received by stored to said first memory,

wherein, in the case where the first controller receives data from the host system, the first controller stores the data in the first memory and the second memory and sends a response to the host system, and then the first controller transfers the data stored in the second memory to the third memory.

10. (Cancelled)

11. (Currently Amended) A storage system ~~according to claim 10, further~~ comprising:

a disk drive;

a first cache memory for temporarily storing data sent from a host system so as to be written on the disk drive;

a second cache memory for storing a duplicate of the data to be written on the disk drive;

a FIFO buffer for temporarily storing the duplicate of the data sent from the host system to transfer the duplicate of the data to the second cache memory,

a first power unit connected to the first cache memory; and

a second power unit connected to the second cache memory, the second power unit being independent from the first power unit,

wherein, at the time when the data sent from the host system are stored in the first cache memory and the duplicate of the data sent from the host system are stored in the FIFO buffer, the host system is informed about the completions of data writing,

wherein the FIFO buffer is connected to the second power unit.

12. (Currently Amended) A storage system ~~according to claim 10, further~~ comprising:

a disk drive;

a first cache memory for temporarily storing data sent from a host system so as to be written on the disk drive;

a second cache memory for storing a duplicate of the data to be written on the disk drive;

a FIFO buffer for temporarily storing the duplicate of the data sent from the host system to transfer the duplicate of the data to the second cache memory,

a first power unit connected to the first cache memory;

a second power unit connected to the second cache memory, the second power unit being independent from the first power unit; and

a third power unit connected to the FIFO buffer, the third power unit being independent from the first power unit₁,

wherein, at the time when the data sent from the host system are stored in the first cache memory and the duplicate of the data sent from the host system are stored in the FIFO buffer, the host system is informed about the completions of data writing.

13. (Currently Amended) A storage system ~~according to claim 10, further~~ comprising:

_____ a disk drive;

_____ a first cache memory for temporarily storing data sent from a host system so as to be written on the disk drive;

_____ a second cache memory for storing a duplicate of the data to be written on the disk drive;

_____ a FIFO buffer for temporarily storing the duplicate of the data sent from the host system to transfer the duplicate of the data to the second cache memory,

_____ a first power unit connected to the first cache memory; and

_____ a second power unit connected to the second cache memory, the second power unit being independent from the first power unit,

_____ wherein, at the time when the data sent from the host system are stored in the first cache memory and the duplicate of the data sent from the host system are stored in the FIFO buffer, the host system is informed about the completions of data writing,

_____ wherein the FIFO buffer is connected to the first power unit and provided with a battery for feeding power to the FIFO buffer in place of the first power unit in case of failure in the first power unit.

14. (Currently Amended) A storage system according to claim 11, further comprising a data remaining indicator for indicating whether or not all the duplicate of the data sent from the host system has been transferred from the FIFO buffer to the second cache memory.

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15. (Cancelled)